

| L Number | Hits | Search Text | DB | Time stamp |
|----------|------|---|---|---------------------|
| - | 3238 | ((address near4 transit\$3 near4 detect\$3) ATD) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/04/13 13:23 |
| - | 0 | ((sense adj2 (amp amplifier)) with (reset adj3 signal)) and ((control\$5 adj3 circuit) with set\$4 with latch) and ((control\$5 adj3 circuit) with reset\$4 with latch) and ((address near4 transit\$3 near4 detect\$3) ATD)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/04/13 13:23 |
| - | 24 | ((sense adj2 (amp amplifier)) with (reset adj3 signal)) and ((control\$5 adj3 circuit) with set\$4 with latch) and ((control\$5 adj3 circuit) with reset\$4 with latch) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/04/13 14:21 |
| - | 135 | ((control\$5 adj3 circuit) with (set\$4 adj4 latch)) and ((control\$5 adj3 circuit) with (reset\$4 adj4 latch)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/04/13 14:22 |
| - | 8 | ((address near4 transit\$3 near4 detect\$3) ATD)) and ((control\$5 adj3 circuit) with (set\$4 adj4 latch)) and ((control\$5 adj3 circuit) with (reset\$4 adj4 latch)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 10:59 |
| - | 5312 | ((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:00 |
| - | 37 | ((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch)) and ((sense adj2 (amp amplifier)) with generat\$3 with (reset adj3 signal)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:04 |
| - | 9 | control\$4 same ((latch (flip adj flop)) with set) same ((latch (flip adj flop)) with reset) same ((latch (flip adj flop)) with (metastable (meta adj stable))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:16 |
| - | 10 | ((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch)) and (output with (metastab\$5 (meta adj stab\$5))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:25 |
| - | 4560 | (latch (flip adj flop)) with set with reset with (active high hi asserted on) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:28 |
| - | 401 | (latch (flip adj flop)) with (set near3 reset) adj4 (active high hi asserted on) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:28 |
| - | 401 | (latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:53 |
| - | 0 | ((latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on))) same (metastab\$5 (meta adj stab\$5)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:29 |

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| - | 6 | ((latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on))) and (metastab\$5 (meta adj stab\$5)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:54 |
| - | 1616 | (latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:54 |
| - | 27 | ((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:54 |
| - | 21 | ((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:54 |
| - | 6 | ((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:54 |
| - | 21 | ((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 11:54 |
| - | 355 | (sense adj (amp amplifier)) with (reset adj3 signal) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:08 |
| - | 14 | ((sense adj (amp amplifier)) with (reset adj3 signal)) with (read\$3 with cell) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:08 |
| - | 133 | (sense adj (amp amplifier)) with generat\$3 with (reset adj3 signal) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:08 |
| - | 25 | (sense adj (amp amplifier)) near5 generat\$3 near5 (reset adj3 signal) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:12 |
| - | 11 | (sense adj (amp amplifier)) adj5 generat\$3 adj5 (reset) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:27 |
| - | 80 | (sense adj (amp amplifier)) adj5 (reset adj2 signal) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:27 |

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| - | 58 | (sense adj (amp amplifier)) adj3 (reset adj2 signal) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:30 |
| - | 24 | (sense adj (amp amplifier)) with (reset adj2 signal) with (read\$3 adj2 operation) | USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:30 |
| - | 6 | (sense adj (amp amplifier) adj3 reset adj3 signal) with (controller (control adj (circuit unit))) | USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/11 14:46 |